



RESULT REGISTER FOR B.E EXAMINATION HELD IN ELECTRONICS & TELECOMMUNICATION ENGINEERING FOR 5th SEMESTER EXAM

COLLEGE : GOA COLLEGE OF ENGINEERING

SEAT No	P R No	GENDER	Attempts	NAME OF CANDIDATE	PAPER DESCRIPTION	THEORY	SESSIONAL TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	REMARKS
203	201610774	M	1	ABHISHEK BARGE	Digital Signal Processing	56	10	22	88 P			
					Transmission Lines and Antennas	50	15		65 P			
					Control System Engineering	55	12		67 P			
					Embedded Systems	58	12		70 P	13 P		
					VLSI Design and Technology	65	17		82 P	24 P		
					Analog Communication	51	15		66 P		18 P	
											493	P
204	201610652	M	1	AIGAL ABHAY CHANDRAKANT	Digital Signal Processing	43	11	22	76 P			
					Transmission Lines and Antennas	35	\$5 14		49 P	\$5		
					Control System Engineering	42	14		56 P			
					Embedded Systems	46	11		57 P	10 P		
					VLSI Design and Technology	62	21		83 P	20 P		
					Analog Communication	29	12		41 F		17 P	
											409	\$5 F
205	201704559	F	1	AKALWADI ASHVINI SATYAPPA	Digital Signal Processing	26	10	22	58 F			
					Transmission Lines and Antennas	17	15		32 F			
					Control System Engineering	16	15		31 F			
					Embedded Systems	55	15		70 P	3 F		
					VLSI Design and Technology	52	15		67 P	16 P		
					Analog Communication	38	\$2 10		48 P	\$2	13 P	
											338	\$2 F
206	201610533	F	1	AKSHADA SHIVANAND BANDEKAR	Digital Signal Processing	64	16	23	103 P			
					Transmission Lines and Antennas	64	21		85 P			
					Control System Engineering	60	20		80 P			
					Embedded Systems	79	19		98 P	22 P		
					VLSI Design and Technology	84	23		107 P	25 P		
					Analog Communication	77	21		98 P		22 P	
											640	P
207	201610536	F	1	ATHALYE JANHAVI SHRIRAM	Digital Signal Processing	67	14	23	104 P			
					Transmission Lines and Antennas	40	21		61 P			
					Control System Engineering	24	14		38 F			
					Embedded Systems	65	18		83 P	19 P		
					VLSI Design and Technology	70	21		91 P	20 P		
					Analog Communication	41	17		58 P		19 P	
											493 #10	F
208	201704560	M	1	BANAULIKAR AKASH ARUN	Digital Signal Processing	60	13	24	97 P			
					Transmission Lines and Antennas	35	\$5 15		50 P	\$5		
					Control System Engineering	47	14		61 P			
					Embedded Systems	55	15		70 P	25 P		
					VLSI Design and Technology	63	11		74 P	25 P		
					Analog Communication	36	\$4 15		51 P	\$4	15 P	
											468	\$9 P
209	201610544	F	1	BHAT NIDHI SURENDRA	Digital Signal Processing	47	19	23	89 P			
					Transmission Lines and Antennas	53	24		77 P			
					Control System Engineering	72	18		90 P			
					Embedded Systems	72	18		90 P	20 P		
					VLSI Design and Technology	58	17		75 P	25 P		
					Analog Communication	66	22		88 P		20 P	
											574	P
210	201610545	M	1	CARVALHO JOEL PIO	Digital Signal Processing	50	10	20	80 P			
					Transmission Lines and Antennas	44	14		58 P			
					Control System Engineering	40	15		55 P			
					Embedded Systems	60	12		72 P	18 P		
					VLSI Design and Technology	72	14		86 P	25 P		
					Analog Communication	60	18		78 P		15 P	
											487	P
211	201704562	M	1	CHAUDHARY SUBHAN KARIMSAB	Digital Signal Processing	0	1	22	23 A			
					Transmission Lines and Antennas	6	11		17 F			
					Control System Engineering	0	8		8 A			
					Embedded Systems	35	\$5 13		48 P	\$5 10 P		
					VLSI Design and Technology	0	11		11 A	A		
					Analog Communication	0	9		9 A		A	
											126	\$5 F
212	201610546	M	1	CHARI BABUSO PRAKASH	Digital Signal Processing	58	10	20	88 P			
					Transmission Lines and Antennas	44	17		61 P			
					Control System Engineering	35	\$5 14		49 P	\$5		
					Embedded Systems	49	13		62 P	10 P		
					VLSI Design and Technology	63	15		78 P	22 P		
					Analog Communication	42	13		55 P		12 P	
											437	\$5 P

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Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college



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COLLEGE : GOA COLLEGE OF ENGINEERING

SEAT No	P R No	GENDER	Attempts	NAME OF CANDIDATE	PAPER DESCRIPTION	THEORY	SESSIONAL TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	REMARKS
213	201610548	M	1	CHODANKAR NIHAL DEVIDAS	Digital Signal Processing	40	11	20	71 P			
					Transmission Lines and Antennas	35	\$5 20		55 P	\$5		
					Control System Engineering	44	19		63 P			
					Embedded Systems	43	11		54 P	18 P		
					VLSI Design and Technology	64	11		75 P	24 P		
					Analog Communication	29	18		47 F		18 P	
											425	\$5 F
214	201610549	F	1	CHODANKER RUTOOJA RAJU	Digital Signal Processing	67	10	20	97 P			
					Transmission Lines and Antennas	45	23		68 P			
					Control System Engineering	56	17		73 P			
					Embedded Systems	62	14		76 P	24 P		
					VLSI Design and Technology	70	18		88 P	24 P		
					Analog Communication	40	15		55 P		22 P	
											527	P
215	201610550	M	1	CHOPDAR ZAMEER HAMEED	Digital Signal Processing	53	13	23	89 P			
					Transmission Lines and Antennas	59	21		80 P			
					Control System Engineering	58	18		76 P			
					Embedded Systems	46	17		63 P	18 P		
					VLSI Design and Technology	65	21		86 P	22 P		
					Analog Communication	66	23		89 P		18 P	
											541	P
216	201610553	F	1	DESAI TANVI SANJAY	Digital Signal Processing	49	10	22	81 P			
					Transmission Lines and Antennas	28	12		40 F			
					Control System Engineering	17	15		32 F			
					Embedded Systems	50	14		64 P	10 P		
					VLSI Design and Technology	64	10		74 P	16 P		
					Analog Communication	50	15		65 P		21 P	
											403	F
217	201610524	M	1	DESHMUKH AKSHAY VINOD	Digital Signal Processing	51	8	21	80 P			
					Transmission Lines and Antennas	47	22		69 P			
					Control System Engineering	42	16		58 P			
					Embedded Systems	61	20		81 P	10 P		
					VLSI Design and Technology	57	19		76 P	18 P		
					Analog Communication	46	13		59 P		16 P	
											467	P
218	201610554	M	1	DESSAI DIGVIJAY MARUTI	Digital Signal Processing	28	11	22	61 F			
					Transmission Lines and Antennas	35	\$5 16		51 P	\$5		
					Control System Engineering	40	13		53 P			
					Embedded Systems	51	6		57 P	11 P		
					VLSI Design and Technology	52	10		62 P	18 P		
					Analog Communication	51	10		61 P		21 P	
											395	\$5 F
219	201610557	M	1	DHARGALKAR SANKIL SAKHARAM	Digital Signal Processing	24	10	20	54 F			
					Transmission Lines and Antennas	44	16		60 P			
					Control System Engineering	35	\$5 16		51 P	\$5		
					Embedded Systems	49	14		63 P	11 P		
					VLSI Design and Technology	50	14		64 P	20 P		
					Analog Communication	40	18		58 P		14 P	
											395	\$5 F
220	201610559	M	1	DINIZ KEAGAN WILLIAM	Digital Signal Processing	41	11	23	75 P			
					Transmission Lines and Antennas	62	16		78 P			
					Control System Engineering	40	13		53 P			
					Embedded Systems	66	10		76 P	10 P		
					VLSI Design and Technology	76	16		92 P	23 P		
					Analog Communication	53	22		75 P		20 P	
											502	P
221	201705120	F	1	DYAPUR SHILPA SHIVANAND	Digital Signal Processing	48	8	23	79 P			
					Transmission Lines and Antennas	20	13		33 F			
					Control System Engineering	18	13		31 F			
					Embedded Systems	43	13		56 P	10 P		
					VLSI Design and Technology	66	13		79 P	20 P		
					Analog Communication	40	11		51 P		13 P	
											372	F
222	201704558	F	1	EKAWADE PRANALEE JAIPRAKASH	Digital Signal Processing	40	10	21	71 P			
					Transmission Lines and Antennas	20	12		32 F			
					Control System Engineering	15	18		33 F			
					Embedded Systems	42	10		52 P	8 F		
					VLSI Design and Technology	60	10		70 P	20 P		
					Analog Communication	53	10		63 P		13 P	
											362	F

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223	201610561	F	1	FERNANDES GRANXA	Digital Signal Processing	35	\$5 10	20	65 P	\$5		
					Transmission Lines and Antennas	44	16		60 P			
					Control System Engineering	35	\$5 14		49 P	\$5		
					Embedded Systems	48	18		66 P	10 P		
					VLSI Design and Technology	64	17		81 P	20 P		
					Analog Communication	51	21		72 P		17 P	
											440	\$10 P
224	201610565	M	1	GAONKAR AKSHAY SHRIKANT	Digital Signal Processing	49	12	20	81 P			
					Transmission Lines and Antennas	54	13		67 P			
					Control System Engineering	62	16		78 P			
					Embedded Systems	58	13		71 P	13 P		
					VLSI Design and Technology	66	13		79 P	24 P		
					Analog Communication	65	20		85 P		16 P	
											514	P
225	201610566	F	1	GAONKAR DEEPTI DHARMA	Digital Signal Processing	55	13	20	88 P			
					Transmission Lines and Antennas	54	20		74 P			
					Control System Engineering	25	14		39 F			
					Embedded Systems	60	11		71 P	10 P		
					VLSI Design and Technology	73	15		88 P	20 P		
					Analog Communication	51	16		67 P		18 P	
											475	P
226	201610691	F	1	GAONKAR MANJITA TANO	Digital Signal Processing	49	11	25	85 P			
					Transmission Lines and Antennas	57	20		77 P			
					Control System Engineering	40	19		59 P			
					Embedded Systems	62	18		80 P	10 P		
					VLSI Design and Technology	68	19		87 P	25 P		
					Analog Communication	51	18		69 P		18 P	
											510	P
227	201610447	F	1	HIEMATH ASHWINI MAHARUDRAYYA	Digital Signal Processing	53	15	23	91 P			
					Transmission Lines and Antennas	59	22		81 P			
					Control System Engineering	43	21		64 P			
					Embedded Systems	78	22		100 P	10 P		
					VLSI Design and Technology	60	23		83 P	25 P		
					Analog Communication	43	21		64 P		20 P	
											538	P
228	201610779	M	1	JOY PAHARI	Digital Signal Processing	68	14	20	102 P			
					Transmission Lines and Antennas	38	\$2 16		54 P	\$2		
					Control System Engineering	41	10		51 P			
					Embedded Systems	49	13		62 P	8 F		
					VLSI Design and Technology	53	18		71 P	22 P		
					Analog Communication	27	15		42 F		14 P	
											426	\$2 F
229	201610573	F	1	KAISUKAR SWETA PRAKASH	Digital Signal Processing	35	\$5 8	20	63 P	\$5		
					Transmission Lines and Antennas	44	17		61 P			
					Control System Engineering	25	14		39 F			
					Embedded Systems	59	12		71 P	11 P		
					VLSI Design and Technology	80	11		91 P	20 P		
					Analog Communication	55	14		69 P		19 P	
											444	\$5 F
230	201610574	F	1	KANEKAR SHWETA NANDKISHOR	Digital Signal Processing	60	20	25	105 P			
					Transmission Lines and Antennas	78	19		97 P			
					Control System Engineering	70	24		94 P			
					Embedded Systems	89	25		114 P	25 P		
					VLSI Design and Technology	74	23		97 P	25 P		
					Analog Communication	71	23		94 P		25 P	
											676	P
231	201610577	M	1	KAVALEKAR ABHIJIT SHASHIKANT	Digital Signal Processing	45	12	21	78 P			
					Transmission Lines and Antennas	41	16		57 P			
					Control System Engineering	22	12		34 F			
					Embedded Systems	61	12		73 P	11 P		
					VLSI Design and Technology	72	16		88 P	20 P		
					Analog Communication	51	9		60 P		12 P	
											433	F
232	201610580	M	1	KENI RAJ ALIAS VISHANT PRAMOD	Digital Signal Processing	48	15	22	85 P			
					Transmission Lines and Antennas	44	13		57 P			
					Control System Engineering	47	20		67 P			
					Embedded Systems	52	21		73 P	24 P		
					VLSI Design and Technology	56	17		73 P	20 P		
					Analog Communication	46	14		60 P		22 P	
											481	P



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233	201610581	F	1	KENI RAVINA RAJIV	Digital Signal Processing	49	15	24	88 P			
					Transmission Lines and Antennas	50	18		68 P			
					Control System Engineering	40	14		54 P			
					Embedded Systems	61	15		76 P	18 P		
					VLSI Design and Technology	64	14		78 P	24 P		
					Analog Communication	60	20		80 P		20 P	
											506	P
234	201704565	M	1	KUMBAAR RAVI	Digital Signal Processing	55	18	24	97 P			
					Transmission Lines and Antennas	40	16		56 P			
					Control System Engineering	44	15		59 P			
					Embedded Systems	69	18		87 P	25 P		
					VLSI Design and Technology	65	11		76 P	24 P		
					Analog Communication	68	17		85 P		17 P	
											526	P
235	201610583	M	1	LOHANI MAYANK RAJESH	Digital Signal Processing	47	19	22	88 P			
					Transmission Lines and Antennas	38 #2	15		53 P #2			
					Control System Engineering	48	18		66 P			
					Embedded Systems	53	16		69 P	12 P		
					VLSI Design and Technology	50	14		64 P	22 P		
					Analog Communication	36 #4	21		57 P #4		17 P	
											448 #10	P
236	201610752	M	1	MANVENDRA SINGH	Digital Signal Processing	55	18	21	94 P			
					Transmission Lines and Antennas	36 \$4	21		57 P \$4			
					Control System Engineering	50	21		71 P			
					Embedded Systems	62	20		82 P	25 P		
					VLSI Design and Technology	75	20		95 P	25 P		
					Analog Communication	48	19		67 P		17 P	
											533	\$4 P
237	201610584	F	1	MARDOLKER POOJA	Digital Signal Processing	49	14	21	84 P			
					Transmission Lines and Antennas	57	18		75 P			
					Control System Engineering	42	15		57 P			
					Embedded Systems	72	18		90 P	16 P		
					VLSI Design and Technology	68	19		87 P	25 P		
					Analog Communication	63	18		81 P		24 P	
											539	P
238	201610587	F	1	METHAR REENA RAJENDRA	Digital Signal Processing	65	14	24	103 P			
					Transmission Lines and Antennas	78	20		98 P			
					Control System Engineering	70	21		91 P			
					Embedded Systems	78	23		101 P	25 P		
					VLSI Design and Technology	83	25		108 P	25 P		
					Analog Communication	70	23		93 P		24 P	
											668	P
239	201610776	M	1	MOHNISH P NAIR	Digital Signal Processing	57	10	22	89 P			
					Transmission Lines and Antennas	50	16		66 P			
					Control System Engineering	30	9		39 F			
					Embedded Systems	46	10		56 P	11 P		
					VLSI Design and Technology	70	14		84 P	20 P		
					Analog Communication	44	9		53 P		16 P	
											434	F
240	201610589	M	1	NABAR VISMAY	Digital Signal Processing	45	18	22	85 P			
					Transmission Lines and Antennas	60	25		85 P			
					Control System Engineering	70	23		93 P			
					Embedded Systems	65	19		84 P	25 P		
					VLSI Design and Technology	63	25		88 P	25 P		
					Analog Communication	64	23		87 P		24 P	
											596	P
241	201705126	F	1	NAIK GANDHALI DATTARAM	Digital Signal Processing	47	7	21	75 P			
					Transmission Lines and Antennas	38 \$2	10		48 P \$2			
					Control System Engineering	23	15		38 F			
					Embedded Systems	64	12		76 P	10 P		
					VLSI Design and Technology	76	10		86 P	16 P		
					Analog Communication	57	12		69 P		16 P	
											434	\$2 F
242	201610593	F	1	NAIK NAVEENA PREMANAND	Digital Signal Processing	48	15	20	83 P			
					Transmission Lines and Antennas	51	19		70 P			
					Control System Engineering	50	21		71 P			
					Embedded Systems	54	11		65 P	15 P		
					VLSI Design and Technology	66	14		80 P	21 P		
					Analog Communication	49	13		62 P		18 P	
											485	P



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243	201610594	F	1	NAIK PALLAVI UMESH	Digital Signal Processing	44	15	25	84 P			
					Transmission Lines and Antennas	65	20		85 P			
					Control System Engineering	64	23		87 P			
					Embedded Systems	80	23		103 P	25 P		
					VLSI Design and Technology	78	20		98 P	25 P		
					Analog Communication	70	24		94 P		25 P	
											626	P
244	201610596	F	1	NAIK PRAJAKTA PUNDALIK	Digital Signal Processing	40	13	21	74 P			
					Transmission Lines and Antennas	46	17		63 P			
					Control System Engineering	42	21		63 P			
					Embedded Systems	51	12		63 P	10 P		
					VLSI Design and Technology	64	14		78 P	18 P		
					Analog Communication	42	16		58 P		20 P	
											447	P
245	201610600	F	1	NAIK SALONI SURENDRA	Digital Signal Processing	63	15	20	98 P			
					Transmission Lines and Antennas	58	14		72 P			
					Control System Engineering	54	18		72 P			
					Embedded Systems	68	11		79 P	17 P		
					VLSI Design and Technology	50	16		66 P	25 P		
					Analog Communication	67	14		81 P		16 P	
											526	P
246	201610601	M	1	NAIK SUYOG ROHIDAS	Digital Signal Processing	35	\$5 11	24	70 P	\$5		
					Transmission Lines and Antennas	57	17		74 P			
					Control System Engineering	46	18		64 P			
					Embedded Systems	51	17		68 P	19 P		
					VLSI Design and Technology	50	12		62 P	20 P		
					Analog Communication	55	19		74 P		18 P	
											469	\$5 P
247	201610766	M	1	NETALKAR AKHIL MOTILAL	Digital Signal Processing	63	21	25	109 P			
					Transmission Lines and Antennas	69	20		89 P			
					Control System Engineering	65	23		88 P			
					Embedded Systems	69	24		93 P	22 P		
					VLSI Design and Technology	63	21		84 P	25 P		
					Analog Communication	55	23		78 P		24 P	
											612	P
248	201610603	F	1	NIDHI RAJEEV NAIK	Digital Signal Processing	67	15	24	106 P			
					Transmission Lines and Antennas	59	21		80 P			
					Control System Engineering	54	22		76 P			
					Embedded Systems	64	16		80 P	13 P		
					VLSI Design and Technology	80	23		103 P	25 P		
					Analog Communication	58	20		78 P		22 P	
											583	P
249	201610604	M	1	PAI G VIKRAM	Digital Signal Processing	25	16	24	65 F			
					Transmission Lines and Antennas	35	\$5 21		56 P	\$5		
					Control System Engineering	40	17		57 P			
					Embedded Systems	58	16		74 P	20 P		
					VLSI Design and Technology	60	18		78 P	24 P		
					Analog Communication	53	18		71 P		18 P	
											463	\$5 F
250	201610959	F	1	PALAN DIKSHA YOGESH	Digital Signal Processing	49	10	22	81 P			
					Transmission Lines and Antennas	35	\$5 23		58 P	\$5		
					Control System Engineering	30	10		40 F			
					Embedded Systems	47	11		58 P	17 P		
					VLSI Design and Technology	65	14		79 P	23 P		
					Analog Communication	47	20		67 P		16 P	
											439	\$5 F
251	201610606	M	1	PARTH TIWARI	Digital Signal Processing	67	17	25	109 P			
					Transmission Lines and Antennas	62	24		86 P			
					Control System Engineering	70	21		91 P			
					Embedded Systems	61	18		79 P	13 P		
					VLSI Design and Technology	74	18		92 P	22 P		
					Analog Communication	72	22		94 P		19 P	
											605	P
252	201610608	F	1	PEREIRA AMY CARLOTA	Digital Signal Processing	42	12	20	74 P			
					Transmission Lines and Antennas	40	19		59 P			
					Control System Engineering	62	22		84 P			
					Embedded Systems	70	19		89 P	17 P		
					VLSI Design and Technology	75	19		94 P	23 P		
					Analog Communication	54	14		68 P		17 P	
											525	P



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253	201704561	F	1	POLEM REDDY SONALI VENKATA RAMANA	Digital Signal Processing	47	10	24	81 P			
					Transmission Lines and Antennas	38	\$2 16		54 P	\$2		
					Control System Engineering	19	17		36 F			
					Embedded Systems	55	16		71 P	12 P		
					VLSI Design and Technology	60	16		76 P	16 P		
					Analog Communication	41	18		59 P		15 P	
											420	\$2 F
254	201610612	M	1	PRABHU DESSAI SATNIL BHUPAL	Digital Signal Processing	50	21	22	93 P			
					Transmission Lines and Antennas	46	16		62 P			
					Control System Engineering	45	20		65 P			
					Embedded Systems	64	15		79 P	21 P		
					VLSI Design and Technology	77	17		94 P	23 P		
					Analog Communication	57	18		75 P		20 P	
											532	P
255	201610755	M	1	PRATIK SINGH NEGI	Digital Signal Processing	35	\$5 11	21	67 P	\$5		
					Transmission Lines and Antennas	26	13		39 F			
					Control System Engineering	23	14		37 F			
					Embedded Systems	41	13		54 P	8 F		
					VLSI Design and Technology	66	13		79 P	17 P		
					Analog Communication	52	12		64 P		15 P	
											380	\$5 F
256	201610875	M	1	RIVONKAR NAVEEN DILIP	Digital Signal Processing	66	23	25	114 P			
					Transmission Lines and Antennas	77	24		101 P			
					Control System Engineering	74	23		97 P			
					Embedded Systems	85	25		110 P	25 P		
					VLSI Design and Technology	78	25		103 P	25 P		
					Analog Communication	70	24		94 P		24 P	
											693	P
257	201610782	F	1	SAHANA RAMESH BELLAD	Digital Signal Processing	51	11	22	84 P			
					Transmission Lines and Antennas	50	14		64 P			
					Control System Engineering	49	16		65 P			
					Embedded Systems	62	13		75 P	10 P		
					VLSI Design and Technology	80	16		96 P	20 P		
					Analog Communication	33	11		44 F		14 P	
											472	P
258	201610625	M	1	SAMANT KANISHK SHAMSUNDAR	Digital Signal Processing	75	20	25	120 P			
					Transmission Lines and Antennas	69	21		90 P			
					Control System Engineering	84	19		103 P			
					Embedded Systems	77	21		98 P	25 P		
					VLSI Design and Technology	88	25		113 P	25 P		
					Analog Communication	76	24		100 P		24 P	
											698	P
259	201610615	F	1	SAWAIKAR ESHA NARENDRA	Digital Signal Processing	40	10	22	72 P			
					Transmission Lines and Antennas	41	18		59 P			
					Control System Engineering	44	16		60 P			
					Embedded Systems	48	15		63 P	10 P		
					VLSI Design and Technology	73	17		90 P	20 P		
					Analog Communication	37	\$3 18		55 P	\$3	18 P	
											447	\$3 P
260	201610617	M	1	SAWANT ABHINAV ANKUSH	Digital Signal Processing	67	17	25	109 P			
					Transmission Lines and Antennas	82	23		105 P			
					Control System Engineering	72	24		96 P			
					Embedded Systems	85	23		108 P	25 P		
					VLSI Design and Technology	71	22		93 P	25 P		
					Analog Communication	58	19		77 P		22 P	
											660	P
261	201610619	M	1	SAWANT SAHIL CHANDRASHEKHAR	Digital Signal Processing	59	11	23	93 P			
					Transmission Lines and Antennas	53	18		71 P			
					Control System Engineering	42	15		57 P			
					Embedded Systems	51	12		63 P	21 P		
					VLSI Design and Technology	60	15		75 P	24 P		
					Analog Communication	53	12		65 P		18 P	
											487	P
262	201610621	M	1	SHAIKH ABDUL KADER	Digital Signal Processing	43	8	22	73 P			
					Transmission Lines and Antennas	35	\$5 14		49 P	\$5		
					Control System Engineering	35	\$5 12		47 P	\$5		
					Embedded Systems	40	14		54 P	10 P		
					VLSI Design and Technology	52	11		63 P	20 P		
					Analog Communication	18	15		33 F		12 P	
											361	\$10 F



RESULT REGISTER FOR B.E EXAMINATION HELD IN ELECTRONICS & TELECOMMUNICATION ENGINEERING FOR 5th SEMESTER EXAM

COLLEGE : GOA COLLEGE OF ENGINEERING

SEAT No	P R No	GENDER	Attempts	NAME OF CANDIDATE	PAPER DESCRIPTION	THEORY	SESSIONAL TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	REMARKS
263	201610623	F	1	SHETYE NAMRATA RAMESH	Digital Signal Processing	50	15	23	88 P			
					Transmission Lines and Antennas	67	20		87 P			
					Control System Engineering	62	21		83 P			
					Embedded Systems	76	25	101 P	25 P			
					VLSI Design and Technology	72	24	96 P	25 P			
					Analog Communication	61	16	77 P		15 P		
											597	P
264	201610626	F	1	SHIRODKAR SHRESHA DINESH	Digital Signal Processing	68	16	20	104 P			
					Transmission Lines and Antennas	66	17		83 P			
					Control System Engineering	53	13		66 P			
					Embedded Systems	75	13		88 P	17 P		
					VLSI Design and Technology	71	17		88 P	18 P		
					Analog Communication	53	15		68 P		14 P	
											546	P
265	201610629	M	1	SILVA BENHUR SHAAN	Digital Signal Processing	56	12	23	91 P			
					Transmission Lines and Antennas	60	19		79 P			
					Control System Engineering	46	12		58 P			
					Embedded Systems	77	17		94 P	25 P		
					VLSI Design and Technology	80	17		97 P	24 P		
					Analog Communication	63	17		80 P		21 P	
											569	P
266	201610631	M	1	SINAI SALELKAR VIBHAV MAHESH	Digital Signal Processing	60	13	24	97 P			
					Transmission Lines and Antennas	78	24		102 P			
					Control System Engineering	75	18		93 P			
					Embedded Systems	79	19		98 P	25 P		
					VLSI Design and Technology	75	23		98 P	24 P		
					Analog Communication	65	20		85 P		19 P	
											641	P
267	201711499	M	1	SONTI GIRISH IRANNA	Digital Signal Processing	44	7	22	73 P			
					Transmission Lines and Antennas	47	19		66 P			
					Control System Engineering	17	13		30 F			
					Embedded Systems	56	15		71 P	13 P		
					VLSI Design and Technology	61	11		72 P	20 P		
					Analog Communication	54	17		71 P		18 P	
											434	F
268	201610633	M	1	TALWADKER RAGVIR RAMNATH	Digital Signal Processing	58	17	23	98 P			
					Transmission Lines and Antennas	54	22		76 P			
					Control System Engineering	52	19		71 P			
					Embedded Systems	62	17		79 P	13 P		
					VLSI Design and Technology	60	20		80 P	20 P		
					Analog Communication	60	20		80 P		16 P	
											533	P
269	201610635	M	1	VELIP ADHITYA PANGLO	Digital Signal Processing	41	10	22	73 P			
					Transmission Lines and Antennas	36	\$4 11		47 P	\$4		
					Control System Engineering	24	14		38 F			
					Embedded Systems	40	10		50 P	10 P		
					VLSI Design and Technology	62	12		74 P	18 P		
					Analog Communication	53	12		65 P		13 P	
											388	\$4 F
270	201610637	F	1	VELIP KALPITA B.	Digital Signal Processing	35	\$5 10	21	66 P	\$5		
					Transmission Lines and Antennas	35	\$5 12		47 P	\$5		
					Control System Engineering	26	13		39 F			
					Embedded Systems	60	14		74 P	11 P		
					VLSI Design and Technology	55	15		70 P	18 P		
					Analog Communication	50	19		69 P		12 P	
											406	\$10 F
271	201610638	F	1	ZUVATKAR PRATIDNYA BALU	Digital Signal Processing	51	12	22	85 P			
					Transmission Lines and Antennas	40	20		60 P			
					Control System Engineering	67	21		88 P			
					Embedded Systems	75	20		95 P	22 P		
					VLSI Design and Technology	69	19		88 P	20 P		
					Analog Communication	53	18		71 P		17 P	
											546	P

Read By:

Checked By:

Date: 08 MAR 2019

Assistant Registrar-E(Proff.)

Controller Of Examinations

Registrar

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over  
Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college